

DRIVING CIRCUIT FOR DC/DC CONVERTER

ABSTRACT OF THE DISCLOSURE

A driving circuit comprises an input terminal receiving an input of a PWM signal, a first output terminal connected to a main switch for outputting a low-side driving signal, a second output terminal connected to an active switch for outputting a high-side driving signal, a first branch having a voltage level shifting capacitor and a first buffer connected in series between the input terminal and the second output terminal; and a second branch having a delay circuit and a second buffer connected in series between the input terminal and the first output terminal. When the input of the PWM signal turns from a low level to a high level, the voltage level shifting capacitor transmits the input of PWM signal to the first buffer for turning off the active switch and then triggering the second buffer to turn on the main switch with a short time delay.